

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/581,641	BRIL ET AL.	
Examiner		Art Unit		Page 1 of 1
BRIAN CHEW		2195		

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A US-5,826,082	10-1998	Bishop et al.	718/104
	B US-			
	C US-			
	D US-			
	E US-			
	F US-			
	G US-			
	H US-			
	I US-			
	J US-			
	K US-			
	L US-			
	M US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N				
	O				
	P				
	Q				
	R				
	S				
	T				

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	Narlikar, J. G. Space-Efficient Implementation of Nested Parallelism, ACM SIGPLAN Notices, Association for Computing Machinery, New York, Vol. 32, no. 7 (Jul. 1, 1997), pp. 25-36
V	
W	
X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.